

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: VOSS, Burkart et al.

Int'l Application No.: PCT/EP03/013527

Int'l Filing Date: 02 December 2003

Serial No.:

Group Art Unit:

Filed:

Herewith

Examiner:

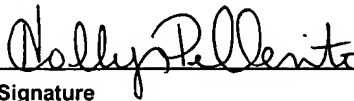
For: Method for the Recognition and/or Correction of Memory Access Errors and  
Electronic Circuit Arrangement for Carrying Out Said Method

Attorney Docket No.: AP10609

Mail Stop PCT  
Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

## CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8 (A))

I hereby certify that this correspondence is, on the date shown below, being

☒ deposited with the United States Postal Service with  
sufficient postage as Express Mail, Post Office to  
Addressee, Mailing Label No. EV 438304237 US,  
addressed to Mail Stop PCT, Commissioner for Patents,  
PO Box 1450, Alexandria, Virginia 22313-1450.☐ transmitted by facsimile to the Patent and Trademark Office,  
to Examiner  
at \_\_\_\_.  
Signature

Date: July 8, 2005

Name: Holly Pellerito

PRELIMINARY AMENDMENT

Dear Sir: